IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Christos John Georgiou, *et. al.*

Group Art Unit: Unknown

Serial No.: 10/604295

Examiner: unknown

Filed: 7-9-2003

For : METHOD AND SYSTEM OF DATA TRANSFER FOR EFFICIENT MEMORY UTILIZATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Sir:

Under provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicants respectfully bring the documents listed on the attached Form PTO-1449 to the attention of the Examiner in charge of the above-identified application. A copy of each listed document is provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the cited references are relevant or material to the claims nor should it be construed as a representation that no other art than that identified exists. They are merely cited as constituting related art of which the applicant is aware.

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It is respectfully requested that these documents be considered by the Examiner and formally made of record in this application.
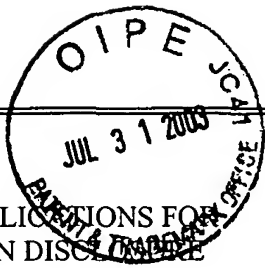
Respectfully submitted,

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FORM PTO-1449 (Modified)	ATTY. DOCKET NO. BUR920030037US1	SERIAL NO. 10/604295
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT: Christos John Georgiou et al.	
(Use several sheets if necessary)	FILING DATE: 7-9-03	GROUP: Unassigned

REFERENCE DESIGNATION   U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		WO9419748A2	9/01/1994					

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

		PARADISE: An Advanced Featured Parallel File System by Maciej Brodowicz and Olin Johnson
		IBM TDB/DOSS, RD n444 04/2001 Article 16 Page 654, "Fast, Shared, Memory With Multiple Output Queues for Variable Length Packets, Made from Slow Memory Elements"
		Object Resizing and Reclamation through the use of hardware, by Witawas Srisa
		IBM TDB n3 08-92 pgs 382-385 "Data Transfer with Virtual Memory Queus"

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.